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**INFORMATION DISCLOSURE CITATION**

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Applicant(s): SHELLEY CHENG

Filing Date: Herewith

Group: Unknown

**U.S. PATENT DOCUMENTS**

*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date
MDT	AA	5,555,140*	8/1997	Haddock	395	200.76	11/02/95
MDT	AB	5,812,792*	9/1998	Haddock et al.	395	200.79	02/26/96
MDT	AC	5,826,015*	10/20/98	Schmidt	713	201	02/20/97
MDT	AD	5,938,771*	8/1999	Williams et al.	713	310	10/30/97
MDT	AE	5,940,771*	8/1999	Gollnick et al.	455	517	10/19/95
MDT	AF	6,101,608*	8/2000	Schmidt et al.	713	202	02/20/97
MDT	AG	5,805,816*	09/08/98	Picazo et al.	395	200.53	01/28/97
MDT	AH	5,796,944*	08/18/98	Hill et al.	395	200.8	07/12/95
MDT	AI	5,751,952*	05/12/98	Dai et al.	395	200.8	11/22/96
MDT	AJ	5,615,340*	03/25/97	Dai et al.	395	200.17	07/21/94
	BB						
	CC						
	DD						
	EE						
	FF						

**FOREIGN PATENT DOCUMENTS**

*Examiner Initials		Document Number	Date	Country	Class	Subclass	Translation YES NO
	AP						
	AQ						
	AR						

**OTHER DOCUMENTS**

AS	National Semiconductor CompactRISC™ 32-Bit Architecture; National Semiconductor Corporation (1997)*
AT	Network PC System Design Guidelines; Compaq Computer Corporation et al.; pages 1-152 (August 5, 1997)*

Examiner

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Date Considered

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